

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Write application of:  
Kimikatsu SHOJI et al.

Appl. No.: 09/377,740

Confirmation No.: 8263

Filed: August 20, 1999

For: SEMICONDUCTOR  
INTEGRATED CIRCUIT AND  
METHOD OF MANUFACTURING  
THE SAME

Art Unit: 2811

Examiner: Not Yet Assigned

Atty. Docket No.: 32014-150502

Customer No.

26694

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**Second Request for Corrected Filing Receipt**

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Sir:

Submitted herewith is a marked-up copy of the corrected filing receipt with a request that a corrected filing receipt be issued reflecting the title to read as follows:

**SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF  
MANUFACTURING THE SAME**

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
09/377,740	08/20/1999	2811	890	32014-150502	2	7	2

CONFIRMATION NO. 8263

CORRECTED FILING RECEIPT



\*OC000000012845394\*

26694  
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Date Mailed: 06/02/2004

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## Applicant(s)

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## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 234333/1998 08/20/1998

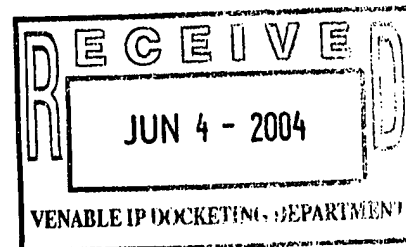
If Required, Foreign Filing License Granted: 09/02/1999

CPA filed on: 06/28/2001

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No



Title

INTEGRATED

SEMICONDUCTOR [INTEGRATED] CIRCUIT AND METHOD OF MANUFACTURING THE SAME

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